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20999 7590 06/30/2004

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10/04/2004 LWONDIN2 00000013 10699202

01 FC:1501	1330.00 DP
02 FC:1504	300.00 DP
03 FC:8001	24.00 DP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/699,202	10/31/2003	Hideaki Aochi	790001-2039	3520

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCORPORATING MEMORY CELL TRANSISTOR AND LOGIC TRANSISTOR, AND METHOD OF MANUFACTURING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	09/30/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
WOJCIECHOWICZ, EDWARD JOSEPH	2815	257-330000

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2
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Kabushiki Kaisha Toshiba

Tokyo, Japan

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Reg. #39,440 Sept. 30, 2004

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Attorney Docket No. : 790001-2039

Title : ***SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
INCORPORATING MEMORY CELL TRANSISTOR
AND LOGIC TRANSISTOR, AND METHOD OF
MANUFACTURING THE SAME***

Serial No. : 10/699,202

Filed : October 31, 2003

Applicant : Hideaki AOCHI

Mailed : Via Express Mail Label # EV206806492US on September 30, 2004

I hereby certify that the Issue Fee Transmittal and a check in the amount of \$1,654.00 for the above-referenced application are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and addressed to:

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Charles Jackson
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